

EAST

	L #	Hits	Search Text	DBs	Time Stamp
1	L1	2906	charge adj injection	USPAT	2000/02/12 10:01
2	L3	29	1 and (shunt\$3 near3 current)	USPAT	2000/02/12 10:11
3	L5	9	1 AND (SHUNT\$3 NEAR5 CURRENT) not 3	USPAT	2000/02/12 10:12
4	L6	9935	SHUNT\$3 NEAR5 CURRENT	USPAT	2000/02/12 10:13
5	L7	2380	6 with (output or load)	USPAT	2000/02/12 10:14
6	L8	221	7 same (current adj source)	USPAT	2000/02/12 10:14
7	L9	103	8 same (transistor or pmos or nmos)	USPAT	2000/02/12 10:15
8	L10	45	8 same (switch or "p-mos" or "n-mos")	USPAT	2000/02/12 10:15
9	L11	131	9 or 10	USPAT	2000/02/12 10:16
10	L13	42	11 same capacit\$4	USPAT	2000/02/12 10:16
11	L14	89	11 not 13	USPAT	2000/02/12 10:23
12	L16	24	14 and gate and drain and source	USPAT	2000/02/12 10:31
13	L18	65	14 not 16	USPAT	2000/02/12 10:41
14	L19	118	8 not 9	USPAT	2000/02/12 10:43
15	L21	28	19 same capacit\$4	USPAT	2000/02/12 10:47
16	L22	90	19 not 21	USPAT	2000/02/12 10:47
17	L24	19	22 and gate and source and drain	USPAT	2000/02/12 10:50
18	L26	71	22 not 24	USPAT	2000/02/12 11:02
19	L27	2159	7 not 8	USPAT	2000/02/12 11:02
20	L28	257	27 and 327/\$.ccls.	USPAT	2000/02/12 11:03
21	L29	194	28 and capacit\$4	USPAT	2000/02/12 11:03
22	L31	64	29 and gate and drain and source	USPAT	2000/02/12 11:13
23	L33	130	29 not 31	USPAT	2000/02/12 11:27
24	L35	63	28 not 29	USPAT	2000/02/12 11:34
25	L36	256	"current source switching"	USPAT	2000/02/12 11:35

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	L #	Hits	Search Text	DBs	Time Stamp
26	L38	31	36 adj circuit	USPAT	2000/02/12 11:43
27	L39	225	36 not 38	USPAT	2000/02/12 11:43
28	L41	22	6 and 39	USPAT	2000/02/12 11:45
29	L43	51	36 and 327/\$.ccls. not (38 or 41)	USPAT	2000/02/12 11:53
30	L44	152	39 not (41 or 43)	USPAT	2000/02/12 11:54
31	L46	29	44 and gate and drain and source	USPAT	2000/02/12 11:59
32	L47	0	1 and 36	USPAT	2000/02/12 11:59
33	L48	1303	current with (bypass\$3) with (output or load)	USPAT	2000/02/12 12:00
34	L49	0	current with (by adj pass\$3) with (output or load)	USPAT	2000/02/12 12:03
35	L51	4	1 and 48	USPAT	2000/02/12 12:04
36	L52	408	48 and "current source"	USPAT	2000/02/12 12:05
37	L53	374	52 not (3 or 5 or 8 or 28 or 38 or 41 or 43 or 46 or 51)	USPAT	2000/02/12 12:06
38	L55	95	53 and gate and source and drain	USPAT	2000/02/12 12:18
39	L56	213	53 and transistor not 55	USPAT	2000/02/12 12:18
40	L58	41	56 and 327/\$.ccls.	USPAT	2000/02/12 12:22
41	L59	1076	divert\$3 with current with (load or output)	USPAT	2000/02/12 12:23
42	L61	1	1 and 59	USPAT	2000/02/12 12:23
43	L62	509	59 and "current source"	USPAT	2000/02/12 12:23
44	L63	464	62 not (51 or 5 or 38 or 41 or 3 or 43 or 8 or 28 or 46 or 55 or 58 or 61)	USPAT	2000/02/12 12:25
45	L64	105	63 and gate and drain and source	USPAT	2000/02/12 12:25
46	L65	375	63 and transistor	USPAT	2000/02/12 12:25
47	L66	102	64 and 65	USPAT	2000/02/12 12:25
48	L68	35	66 and 327/\$.ccls.	USPAT	2000/02/12 12:30

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49	L70	67	66 not 68	USPAT	2000/02/12 12:36
50	L71	273	65 not 66	USPAT	2000/02/12 12:36
51	L72	206	71 and capacit\$4	USPAT	2000/02/12 12:37
52	L74	47	72 and 327/\$.ccls.	USPAT	2000/02/12 12:41
53	L75	159	72 not 74	USPAT	2000/02/12 12:41
54	L77	22	75 and inject\$3	USPAT	2000/02/12 12:47
55	L79	137	75 not 77	USPAT	2000/02/12 12:51